1. Author(s)

2. Title of article

3. Journal/Conference (include volume and issue for journals)

4. Volume and Number

5. Page numbers

6. Month and Year

7. Abstract

**Power invariant secure IC design methodology using reduced complementary dynamic and differential logic**

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## A VLSI design flow for secure side-channel attack resistant ICs

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## Security-aware design flow for 2.5D IC technology

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## Study on PUF based secure protection for IC design

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## Scan design and secure chip [secure IC testing]

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## Multi-threshold dual-spacer dual-rail delay-insensitive logic (MTD3L): a low overhead secure IC designmethodology

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## Design of phone card IC with security and self-test features

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## Is the Secure IC camouflaging really secure?

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## Security Rule Checking in IC Design

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## Design and implement of the Bitcoin payment system based on IC card

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## A Secure Reconfigurable Crypto IC With Countermeasures Against SPA, DPA, and EMA

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